




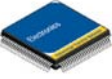
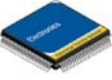

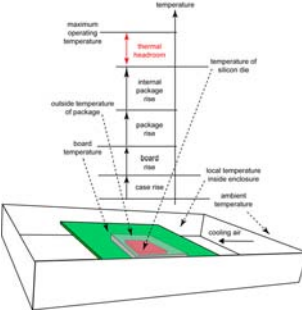



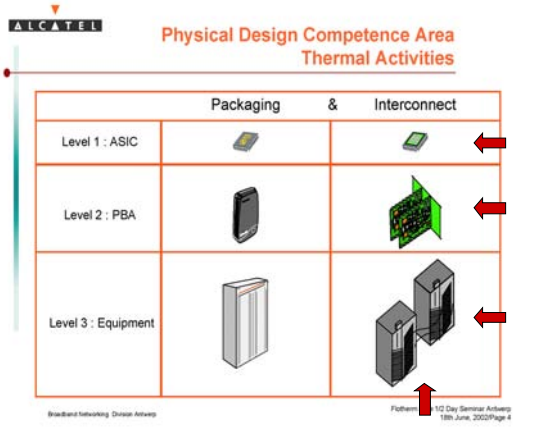


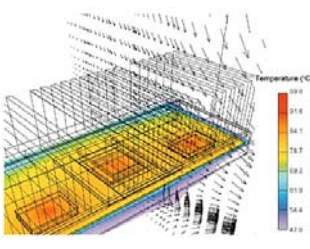

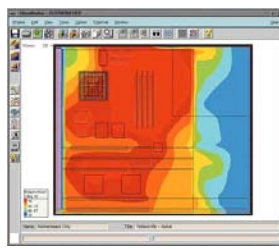
How thermal design is tackled

 <h3>The need for thermal management</h3> <ul style="list-style-type: none">▪ How big is the problem?▪ What heat does the circuit▪ What happens if we ignore the challenge? ▪ How thermal design is tackled <p>Electronics KTN – Knowledge For Growth</p>	<p>By now you have seen the scale of the problem, and hopefully been encouraged not to ignore the challenge! So how do we do something about it? This is what this short section is all about ...</p>
 <h3>Managing thermal design</h3>  <p>a very light touch!</p> <ul style="list-style-type: none">▪ Place of thermal analysis in electronic design<ul style="list-style-type: none">▪ managing thermal risk▪ thermal design strategies <p>Electronics KTN – Knowledge For Growth</p>	<p>... but it is a very light touch, because we will be returning to the topic in the very last presentation.</p> <p>Thermal analysis in electronic design is all about managing thermal risk, and having strategies to deal with thermal challenges. We are putting in this effort because we want our product to have a better chance of life, and to do this we need to quantify the thermal aspects of our design.</p>
 <h3>Managing thermal design</h3> <ul style="list-style-type: none">▪ Place of thermal analysis in electronic design<ul style="list-style-type: none">▪ managing thermal risk▪ thermal design strategies▪ Implementing thermal analysis in product development<ul style="list-style-type: none">▪ a thermal design methodology▪ concept development phase▪ detailed design phase▪ hardware test phase▪ risk management <p>Electronics KTN – Knowledge For Growth</p>	<p>Typically the process of implementing thermal analysis in product development is going to be a management-driven process that starts right at the very earliest stages of concept development. In fact, it needs to start even before that, with agreement as to how thermal design in general should be tackled!</p> <p>Having considered thermal challenges from the beginning of concept development, this needs to be followed through the detailed design phase, taking care to keep thermal design synchronised with electronic design. And finally, thermal analysis needs to be tested against reality when the product is actually built. Overall, we can't remove every element of risk, but we can ensure that the product is designed for correct function both during normal life and during life events that we might reasonably anticipate.</p>

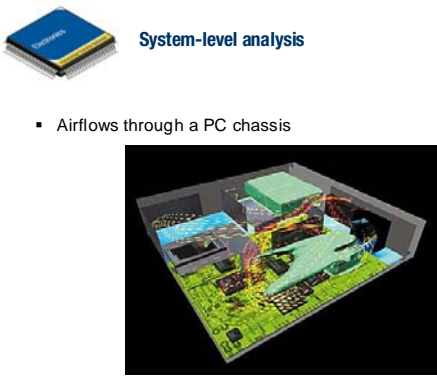
How thermal design is tackled

<div style="border-bottom: 2px solid yellow; margin-bottom: 10px;"></div> <div style="display: flex; align-items: center; margin-bottom: 10px;">  <div style="margin-left: 10px;"> <h3>How thermal design is tackled</h3> </div> </div> <ul style="list-style-type: none"> ▪ New design concept <ul style="list-style-type: none"> ▪ package changes ▪ material improvements ▪ Start early during the design <ul style="list-style-type: none"> ▪ assess the extent of the problem ▪ model thermally as well as build electrical prototype <p style="text-align: right; font-size: small; margin-top: 20px;">Electronics KTN – Knowledge For Growth</p>	<p>As soon as we get our first glimpse of the latest design, we should be evaluating it thermally and assessing the options for changes to the way in which the product is made and the materials used.</p> <p>The first task is to assess the extent of the problem – how big is it? – and we are going to model the product rather than build a prototype, because it is considerably cheaper to model something on a computer than to cut metal, which used to be the only way of doing it.</p>
<div style="border-bottom: 2px solid yellow; margin-bottom: 10px;"></div> <div style="display: flex; align-items: center; margin-bottom: 10px;">  <div style="margin-left: 10px;"> <h3>How thermal design is tackled</h3> </div> </div> <ul style="list-style-type: none"> ▪ New design concept <ul style="list-style-type: none"> ▪ package changes ▪ material improvements ▪ Start early during the design <ul style="list-style-type: none"> ▪ assess the extent of the problem ▪ model thermally as well as build electrical prototype ▪ Role of intuition <ul style="list-style-type: none"> ▪ dangers of intuition ▪ levels of modelling <p style="text-align: right; font-size: small; margin-top: 20px;">Electronics KTN – Knowledge For Growth</p>	<p>We will be using the intuition borne of experience and knowledge of good practice to inform our evaluation of the results of modelling, so that practice and theory stack up, because things aren't always what we expect.</p> <p>You can gain a lot from talking to people who have been there before. They tend to be a bit on the conservative side, and might even say “Been there, done that, shouldn't do that if I were you”! But you should at least ask yourself: “Well, why are they saying that?” There might just be some helpful content in the experience of others!</p>
<div style="border-bottom: 2px solid yellow; margin-bottom: 10px;"></div> <div style="display: flex; align-items: center; margin-bottom: 10px;">  <div style="margin-left: 10px;"> <h3>Aim to increase “thermal head-room”</h3> </div> </div>  <p style="text-align: right; font-size: small; margin-top: 10px;">Electronics KTN – Knowledge For Growth</p>	<p>Many designers will use prior experience to suggest best practice, or to indicate, by means of “rules of thumb”, shortcuts to conservative design such as the maximum dissipation for a particular enclosure type. However, there are dangers in this approach, particularly when working towards the upper limits of dissipation for a design. For this we may need to carry out at least some calculations, and probably perform some finite element modelling of the product, in order to be certain that there is enough “thermal headroom”, a concept which is illustrated here.</p>
<div style="border-bottom: 2px solid yellow; margin-bottom: 10px;"></div> <div style="display: flex; align-items: center; margin-bottom: 10px;">  <div style="margin-left: 10px;"> <h3>How thermal design is tackled</h3> </div> </div> <ul style="list-style-type: none"> ▪ Holistic <ul style="list-style-type: none"> ▪ reliability ▪ components ▪ materials ▪ testing ▪ Focuses sequentially on <ul style="list-style-type: none"> ▪ packaging ▪ interconnect method ▪ enclosure <p style="text-align: right; font-size: small; margin-top: 20px;">Electronics KTN – Knowledge For Growth</p>	<p>Thermal design has to be holistic. We need to look at the components, at the way that they interconnect, at the materials that we're using. In fact, at every aspect of the design, which in some cases might include the room in which the product is sited. In other cases we may need to look in very great detail at the silicon device that is generating the heat.</p> <p>But, however holistic, thermal design tends to focus first on the packaging and the interconnect, before looking at the enclosure.</p>

How thermal design is tackled

 <p>ALCATEL Physical Design Competence Area Thermal Activities</p> <table border="1"> <thead> <tr> <th></th> <th>Packaging</th> <th>& Interconnect</th> </tr> </thead> <tbody> <tr> <td>Level 1 : ASIC</td> <td></td> <td></td> </tr> <tr> <td>Level 2 : PBA</td> <td></td> <td></td> </tr> <tr> <td>Level 3 : Equipment</td> <td></td> <td></td> </tr> </tbody> </table> <p><small>Broadband Networking, Orange Antwerp</small> <small>110 Day Seminar, Antwerp 18th June, 2002/Slide 4</small></p>		Packaging	& Interconnect	Level 1 : ASIC			Level 2 : PBA			Level 3 : Equipment			<p>It is quite useful to think in terms of the levels from component through to the equipment, as Alcatel do in this illustration. At the first level, we are considering heat generated by the device, and conducted primarily through its package to the outside.</p> <p>At the second level, we have components, modules and boards joined together in varying levels of complexity, where thermal flows are primarily by conduction, but there is also some heat loss through convection.</p> <p>At the third level, we are looking at structures of significant size and primarily at convection as a means of heat transfer. But it's worth pointing out that we can't ignore the electrical interconnect, as this will have implications both for the distribution of heat and for thermal management.</p>
	Packaging	& Interconnect											
Level 1 : ASIC													
Level 2 : PBA													
Level 3 : Equipment													
 <p>How thermal design is tackled</p> <ul style="list-style-type: none"> ▪ Holistic ▪ Focuses on <ul style="list-style-type: none"> ▪ packaging ▪ interconnect method ▪ Operates at all levels <ul style="list-style-type: none"> ▪ from IC package to equipment room ... and everything in between <p style="text-align: right;"><small>Electronics KTN – Knowledge For Growth</small></p>	<p>So thermal design is holistic, it focuses on packaging and interconnect method, and it operates at all levels from the internals of the integrated circuit package through to the equipment room – and everything in between.</p>												
 <p>Package-level analysis</p> <ul style="list-style-type: none"> ▪ Pentium®II Processor & heatsink  <p style="text-align: right;"><small>Electronics KTN – Knowledge For Growth</small></p> <p><small>Steve Addison, Electronics Cooling, January 2000</small></p>	<p>The process starts off by modelling the part on the heat sink ...</p>												
 <p>Board-level analysis</p> <ul style="list-style-type: none"> ▪ Temperatures on a Motorola motherboard  <p style="text-align: right;"><small>Electronics KTN – Knowledge For Growth</small></p> <p><small>Steve Addison, Electronics Cooling, January 2000</small></p>	<p>... it then takes the part on the heat sink and looks at the whole of the motherboard ...</p>												

How thermal design is tackled



System-level analysis

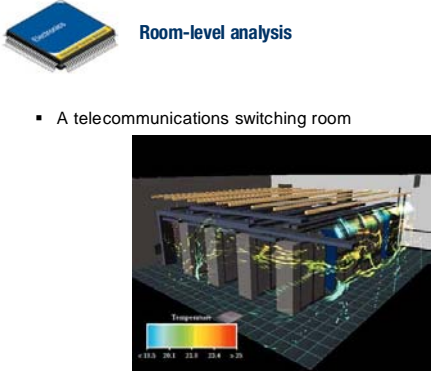
- Airflows through a PC chassis

Steve Addison, *Electronics Cooling*, January 2000

Electronics KTN – Knowledge For Growth

... it then looks at the motherboard in the context of the complete computer chassis in this case. At this stage we are looking in more detail at convection, so this view includes the air flow.

When you examine models of this sort, it becomes clear where the hot spots are likely to be. Intuition will perhaps have told you some of that, but intuition on its own is by no means a substitute for doing things properly.



Room-level analysis

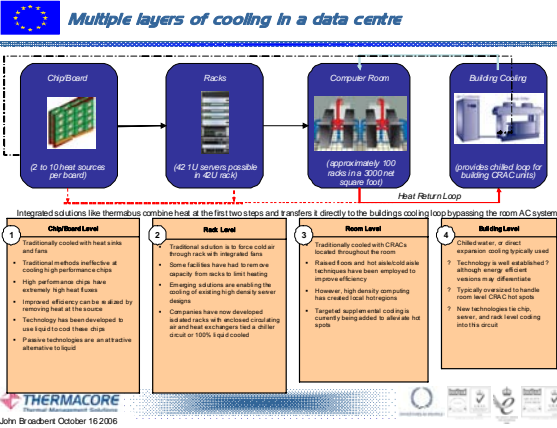
- A telecommunications switching room

Steve Addison, *Electronics Cooling*, January 2000

Electronics KTN – Knowledge For Growth

Finally, we view the whole environment, in this case a telecoms switching room. Here our model can be useful in examining the effect of the different strategies for supplying the room with air. For example, for reasons of dust control, you might want to have the input air coming downwards.

The picture is also a reminder that this isn't really a closed box, but what is happening outside will have an impact on our system. There are many air-conditioning systems that function well during temperate weather, but struggle during the summer months.



Multiple layers of cooling in a data centre

Chip/Board (2 to 10 heat sources per board) → Racks (42 1U servers possible in 42U rack) → Computer Room (approximately 300 racks in a 3000 sq. ft. room) → Building Cooling (provides chilled loop for building CRAC units)

Heat Return Loop

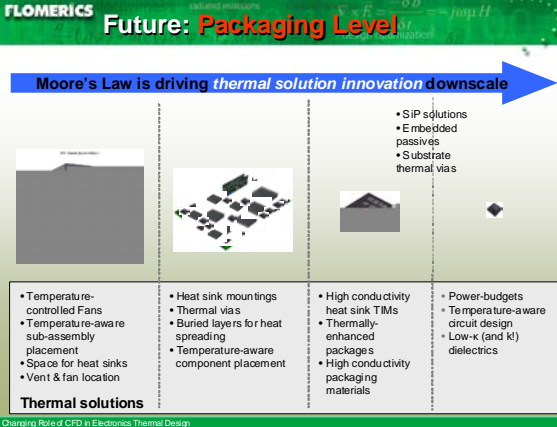
Integrated solutions like thermabus combine heat at the first two steps and transfer it directly to the buildings cooling loop bypassing the room AC system

1 Chip/Board Level	2 Rack Level	3 Room Level	4 Building Level
<ul style="list-style-type: none"> Traditionally cooled with heat sinks and fans Traditional methods ineffective at cooling high performance chips High performance chips have extremely high heat fluxes Improved efficiency can be realized by removing heat at the source Technology has been developed to use liquid to cool these chips Passive technologies are an attractive alternative technique 	<ul style="list-style-type: none"> Traditional solution is to force cold air through racks with integrated fans Some facilities have had to run one capacity from racks to limit heating Emerging solutions are enabling the cooling of existing high density server designs Companies have now developed solid rack with enclosed circulating air and heat exchangers that is either air-cooled or liquid-cooled 	<ul style="list-style-type: none"> Traditionally cooled with CRACs located throughout the room Raised floor and hot aisle/cold aisle techniques have been employed to improve efficiency However, high density computing has created local hotspots Targeted supplemental cooling is currently being added to allow hot spots 	<ul style="list-style-type: none"> Chilled water or steam expansion cooling typically used Technology is well established although energy efficient variations may differentiate Typically covered in terms of room level CRAC hot spots New technologies like chip, server, and rack level cooling into this circuit

John Brabant October 16 2006

This slide reinforces the point that one needs to consider cooling at all levels, from the chip right through to the building. It is a reminder too that there are different solutions for different tasks.

But the original point of this picture is also worth bearing in mind, that there are innovative solutions that allow direct heat transfer, in this case, direct from board level to building cooling by using heat pipe technology.



FLORIMERIC Future: Packaging Level

Moore's Law is driving thermal solution innovation downscale

- SIP solutions
- Embedded passives
- Substrate thermal vias

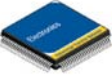

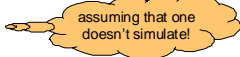

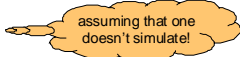

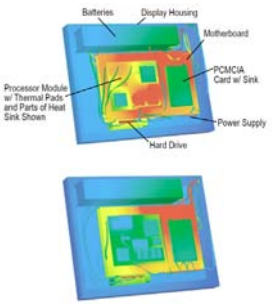
Thermal solutions	Heat sink mountings	High conductivity heat sink TIMs	Power budgets
<ul style="list-style-type: none"> Temperature-controlled Fans Temperature-aware sub-assembly placement Space for heat sinks Vent & fan location 	<ul style="list-style-type: none"> Thermal vias Buried layers for heat spreading Temperature-aware component placement 	<ul style="list-style-type: none"> Thermally enhanced packages High conductivity packaging materials 	<ul style="list-style-type: none"> Temperature-aware circuit design Low-k (and kt) dielectrics

Charging Related CFD in Electronics Thermal Design




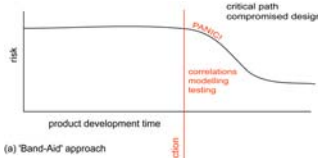
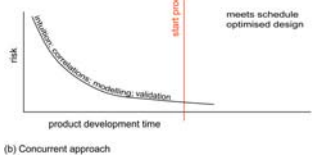


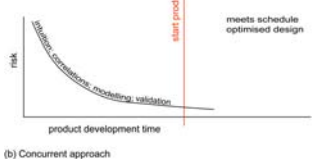
So far we've taken a bottom-up approach to the challenge, but it's equally valid to start with the larger problem and work downwards, finally focusing on chip-level problems, like graphic chips that use more energy than they should!

Such an approach is useful, at least to use in parallel, because it sets the context for the overall solution, and then looks at ways in which design, materials and components can work together for optimal effect.


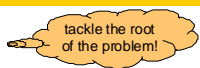


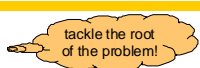




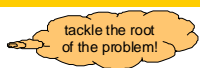

How thermal design is tackled

 <p>How thermal design is tackled</p> <ul style="list-style-type: none"> ▪ Holistic ▪ Focuses on <ul style="list-style-type: none"> ▪ packaging ▪ interconnect method ▪ Operates at all levels <ul style="list-style-type: none"> ▪ from IC package to equipment room ... and everything in between ▪ Manages <ul style="list-style-type: none"> ▪ excess heat ▪ risk <p style="text-align: right; font-size: small;">Electronics KTN – Knowledge For Growth</p>	<p>So our holistic thermal design has considered solutions at all levels, and used a simulation approach to manage excess heat and the associated risk.</p>
 <p>Hidden hot spots!</p>  <ul style="list-style-type: none"> ▪ Unknown until prototypes tested ▪ Engineers forced to over-design <ul style="list-style-type: none"> ▪ "safe" guidelines ▪ more fans, heat sinks, vents ▪ experience is needed <p style="text-align: right; font-size: small;">Electronics KTN – Knowledge For Growth</p>	<p>If you aren't going to simulate, then the situation is going to be unknown until the prototypes have been tested, so typically you are going to have to play safe, adopt conservative guidelines, use more fans, heat sinks and vents, and you will need to draw on experience, either your own or from a colleague who has been there before.</p>
 <p>Hidden hot spots!</p>  <ul style="list-style-type: none"> ▪ Unknown until prototypes tested ▪ Engineers forced to over-design <ul style="list-style-type: none"> ▪ "safe" guidelines ▪ more fans, heat sinks, vents ▪ experience is needed ▪ Results <ul style="list-style-type: none"> ▪ lower average operating temperatures ▪ no insurance against lurking hot spots ▪ unnecessary cost and weight ▪ greater noise and reduced reliability (fans) <p style="text-align: right; font-size: small;">Electronics KTN – Knowledge For Growth</p>	<p>The probable result of doing things without simulation is that you will be operating circuits rather cooler than you need to. However, that's still no insurance that every part of the circuit is going to be cool enough. On average, the whole enclosure might be nice and cool, but that's no good if you have a hot spot next to a sensitive component.</p> <p>If you are being cautious, you might say: "I'll put an extra fan in here anyway". Well, that's not a good idea, because two fans are not necessarily better than one as we'll see in Section 4. Also, if you put an extra fan in, you double the cost, you use more energy, you make the whole system heavier, and you make it noisier.</p>
 <p>Reducing the thermal challenge</p> <ul style="list-style-type: none"> ▪ Original simulation showed four components above max. temperature ▪ Cured using custom heat sink — and without fans!  <p style="text-align: right; font-size: small;">Electronics KTN – Knowledge For Growth</p>	<p>This is an interesting real example of a simulation where the original design had a number of components that were hotter than they were allowed to be. In this case, whilst there were a number of options, the designer chose not to use fans but instead to develop a custom heat sink.</p> <p>This is a very good example of a solution that could be developed by simulation much more easily and at much lower cost than by developing and testing prototypes. In fact, without using simulation, it is unlikely that a passive solution would have been the outcome.</p>

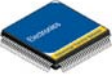
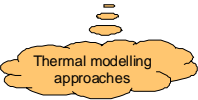

How thermal design is tackled

 <h3>Benefits of thermal modelling</h3> <ul style="list-style-type: none"> Evaluate proposed design changes <ul style="list-style-type: none"> without cutting metal fast enough to encourage "what if?" Can be used to see the effect of likely failure modes <p style="text-align: right; font-size: small;">Electronics KTN – Knowledge For Growth</p>	<p>Thermal simulation has the benefit that one can evaluate proposed design changes without cutting metal, and the process is fast enough to encourage you to play around with the simulation, and say: Well, what happens if I do this? What happens if I move things around? What effect does this have on the thermal distribution and performance? And you can also see what happens if a link in the heat management system fails.</p>																													
 <h3>Thermal Simulation ABCE Board</h3> <p>Simulation results fan failure</p> <ul style="list-style-type: none"> 1 half of the chimney inlet has fixed velocity = 0.5 m/s Junction temperature rise above inlet Inlet temperature = 63 C <table border="1" data-bbox="247 840 614 1064"> <thead> <tr> <th rowspan="2">Component</th> <th colspan="2">Metal core Ended vias</th> <th colspan="2">Multi layer Ended vias</th> </tr> <tr> <th>Fans ON</th> <th>FF</th> <th>Fans ON</th> <th>FF</th> </tr> </thead> <tbody> <tr> <td>ISE OUT</td> <td>35,2 C</td> <td>39,9 C</td> <td>39,2 C</td> <td>47,6 C</td> </tr> <tr> <td>ISE IN</td> <td>34,5</td> <td>39,7</td> <td>38,3</td> <td>46,6</td> </tr> <tr> <td>TMC OUT</td> <td>22,7</td> <td>25,7</td> <td>24,5</td> <td>29,0</td> </tr> <tr> <td>TMC IN</td> <td>22,1</td> <td>25,4</td> <td>23,7</td> <td>28,4</td> </tr> </tbody> </table> <p style="font-size: x-small;">Broadband Networking, Orange Airport Flatham Free 10 Day Seminar, Antwerp 18th June, 2002/Slide 34</p>	Component	Metal core Ended vias		Multi layer Ended vias		Fans ON	FF	Fans ON	FF	ISE OUT	35,2 C	39,9 C	39,2 C	47,6 C	ISE IN	34,5	39,7	38,3	46,6	TMC OUT	22,7	25,7	24,5	29,0	TMC IN	22,1	25,4	23,7	28,4	<p>Such system failure was indeed what Alcatel modelled here, showing the two conditions of fans on and fans failed. In this case, you will notice that they also favoured a conduction-led solution, as a way of keeping the temperature down in the event of fan failure, but in this case they ended up substituting the multi-layer board with a metal-cored laminate.</p>
Component		Metal core Ended vias		Multi layer Ended vias																										
	Fans ON	FF	Fans ON	FF																										
ISE OUT	35,2 C	39,9 C	39,2 C	47,6 C																										
ISE IN	34,5	39,7	38,3	46,6																										
TMC OUT	22,7	25,7	24,5	29,0																										
TMC IN	22,1	25,4	23,7	28,4																										
 <h3>Recommendation: Get started early!</h3>  <p>(a) 'Band-Aid' approach</p>  <p>(b) Concurrent approach</p> <p style="text-align: right; font-size: small;">Electronics KTN – Knowledge For Growth</p>	<p>In order to get the benefits of modelling, the thermal designer should be an integral part of the design team right from the start, rather than wait until the later part of the development cycle. In other words, looking at this comparison of different ways of risk management for thermal design, the designer needs to use a concurrent approach rather than the Band-Aid approach.</p>																													
 <h3>Recommendation: Get started early!</h3>  <p>(a) 'Band-Aid' approach</p>  <p>(b) Concurrent approach</p> <p style="text-align: right; font-size: small;">Electronics KTN – Knowledge For Growth</p>	<p>Another point that these curves reinforce is that informed intuition or simple hand calculation employed early in the design process is sometimes worth more than detailed CFD analysis at the end! We would certainly recommend anyone to get started early with thermal management, using a combination of approaches, including experience, simple calculation and modelling, and to finish the validation before production starts.</p>																													

How thermal design is tackled

 <p>Thinking out of the box</p>   <p>Thanks to Lib in LDN's photostream at http://www.flickr.com/photos/inycreativecorner/</p> <p>Electronics KTN – Knowledge For Growth</p>	<p>As well as making an early start to tackling a thermal management challenge, a second recommendation is to think out of the box, and as far as possible tackle the root of the problem. As we saw, the X-box problem came from trying to package a product that was consuming too much power, and considerable reduction in power was possible without extreme thermal management.</p>
 <p>Thinking out of the box</p>    <ul style="list-style-type: none"> ▪ CherryPal cutting the power <ul style="list-style-type: none"> ▪ \$249, paperback-sized desktop ▪ onboard flash memory ▪ boots in 20 seconds ▪ only 2W — 97% less than typical desktop ▪ Integration a key factor <ul style="list-style-type: none"> ▪ Freescale MPC5121e processor includes network connectivity, multimedia processing and graphics subsystem <p>http://www.edn.com/article/CA6580041.html</p> <p>Electronics KTN – Knowledge For Growth</p>	<p>What CherryPal did was to create the equivalent of a desktop PC, but considerably smaller and consuming only 2W, which is 97% less than a typical desktop. Integration was a key factor in making this possible, ...</p>
 <p>Thinking out of the box</p> <ul style="list-style-type: none"> ▪ Small, cheap, robust, reliable, green ... <i>but</i> ▪ Downsides <ul style="list-style-type: none"> ▪ external peripherals ▪ online processing and storage resources ▪ relatively slow processor ▪ “invisible OS” – browser manages all functions and applications ▪ only pre-installed software <p>Electronics KTN – Knowledge For Growth</p>	<p>... and the product was small, cheap, robust, reliable and green. Of course there are some downsides, such as not being able to install your own software. But, for many users, 2W might be sufficiently attractive to make the compromises acceptable.</p>
 <p>Thinking out of the box</p>   <p>Thanks to Lib in LDN's photostream at http://www.flickr.com/photos/inycreativecorner/</p> <p>Electronics KTN – Knowledge For Growth</p>	<p>If you get started early enough, you have the opportunity of thinking outside the box; if you wait until you have a thermal problem, then you have much more of a challenge!</p>

How thermal design is tackled

 <h3>The need for thermal management</h3> <ul style="list-style-type: none">▪ How big is the problem?▪ What heat does to the circuit▪ What happens if we ignore the challenge?▪ How thermal design is tackled   <p>Electronics KTN – Knowledge For Growth</p>	<p>We said at our introduction that thermal management is often not taken seriously, or even considered at all, and we have seen the potential magnitude of thermal problems. Hopefully by now you have decided not to ignore the challenge, but to tackle thermal design by appropriate modelling approaches, and these are the subject of our subsequent sections.</p> <p>If we tackle thermal design in this structured fashion, we will find that we shrink the size of the thermal problem. It doesn't go away, but it becomes more manageable, and gives us a better product at an affordable cost.</p>
---	---